

[ONSITE] BSC Training Course: Heterogeneous Programming on FPGAs with OmpSs@FPGA@ BSC

Friday, March 22, 2024 - Friday, March 22, 2024

C6 Building

Scientific Program

Day 1 (Friday March, 22nd)

Session 1 / 9:00am – 12:00 pm

09.30h – Intro and welcome

10:00h - OmpSs@FPGA Overview and Internals

10.40h - HLS highlights

11:00h - Case of Study: Step-by-Step Optimization w/ Interactive Quiz

12:00pm 2:00pm – Lunch Break

Session 2 / 2:00pm – 17:30 pm

Only for people attending on site (A5S112), as Zedboard FPGAs will be lent to attendees*

14.00h – Hands-on: Matrix multiply on Zedboard (same case as step-by-step)*

17:30h - Adjourn

END of COURSE