

Programming fo the Intel Xeon Phi

Wednesday, 16 April 2014 15:15 (1h 30m)

This session starts with an introduction to the Intel Xeon Phi architecture. The different memory models and execution models will be presented. The attendees will apply their acquired knowledge on efficiently using this architecture to practical examples and gain first experiences with Xeon Phi.

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Session Classification: Software Engineering for Parallel & Emerging Computing Architectures

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