

**PRACE 2017 Winter
School-Tel Aviv-Fuelling
Scientific Discovery with HPC
Infrastructure**

Report of Contributions

Contribution ID: 0

Type: **not specified**

Introduction: MPI +Open MP

Primary author: Dr TEL ZUR, Guy (BGU)

Presenter: Dr TEL ZUR, Guy (BGU)

Contribution ID: 1

Type: **not specified**

Parallel I/O

Primary author: LUEHRS, Sebastian (JSC)

Presenter: LUEHRS, Sebastian (JSC)

Contribution ID: 2

Type: **not specified**

Performance Analysis and Tuning

Primary author: Dr MOHR, Bernd (Jülich Supercomputing Centre of Forschungszentrum)

Presenter: Dr MOHR, Bernd (Jülich Supercomputing Centre of Forschungszentrum)

Contribution ID: 3

Type: **not specified**

GPU

Primary author: MEINKE, Jan (JSC)

Presenter: MEINKE, Jan (JSC)

Contribution ID: 4

Type: **not specified**

Intel Xeon Phi

Presenter: MEINKE, Jan (JSC)

Contribution ID: 6

Type: **not specified**

LAMMPS

Primary author: Prof. MORDEHAI, Dan (Technion-Israel Inst. of Technology)

Presenter: Prof. MORDEHAI, Dan (Technion-Israel Inst. of Technology)

Contribution ID: 7

Type: **not specified**

Multiprocessing in Python at Node & Cluster Level and cluster level

Primary author: Mr BUTRASHVILY, Mordechai (Tel Aviv University)

Presenter: Mr BUTRASHVILY, Mordechai (Tel Aviv University)

Contribution ID: 8

Type: **not specified**

GPU algorithm design: GPU architecture and programming and methods for adapting algorithms for efficient GPU utilization

Presenter: Prof. RAPAPORT, Dennis (Bar-Ilan University)