

# Co-Design Workshop at EuroHPC Summit Week 2019 Poznan (Poland)

Wednesday 15th May 14h30-19h00,

## **Title: “Co-designing with the European Processor Initiative”**

The co-design with applications is a critical activity in the development of the exascale technology. The European Processor Initiative (EPI) is leading the European effort to develop a competitive solution for the next generation of computing, providing the key components for building Exascale supercomputers based on European hardware and software technologies. This session will bring together the application communities with clear needs for exascale performance and the EPI developers as well as players building pilots based on EPI to discuss about how to collaborate in the co-design effort in view of the integration of the European technologies in pilots. The final goal will be to ensure that the new European technologies are fully exploited and meet the needs of users and applications addressing grand scientific and societal challenges. This session targets the European technology and application developers willing to collaborate in the co-design effort towards European Exascale supercomputers based on EPI technologies.

## **Goals:**

- Identify applications that could drive co-design for EPI and the pilot system projects
- Introduce attendees to co-design for EPI and pilot system ideas leading to supercomputers based on EPI technologies
- Discuss component-level vs. system-level co-design: who does what when.
- Highlight ongoing co-design efforts within EPI - Bring application and technology/system architecture experts together
- Show the current status of the software landscape, and discuss how they can contribute to a coherent effort towards the European exascale supercomputers

**Moderators: Andrea Feltrin, Leonardo Flores Añover (EC)**

## Agenda:

Time	Title	Speakers (tbc)
14:30-14:45	The European roadmap to Exascale	Leonardo Flores
14:45-15:00	The European Processor Initiative (EPI)	Philippe Notton
15:00-15:25	Co-design in EPI	E.Suarez/D.Pleiter
15:25-15:45	Grand challenge applications: technical requirements for the exascale era	Carlo Cavazzoni
15:45-16:15	Pilots and system-level co-design	Jean-Marc Denis/Mateo Valero/Georgios Goumas
16:15-16:30	The Software aspects of the road towards exascale in Europe	Jesus Labarta
16:30-17:00	Coffee break	
17:00-19:00	Summary and Expert Panel	Open discussion
19:00	End	

The last session will be structured around a panel of invited experts. Questions from the audience and from the moderators will address the issues of defining a co-design process, selecting the best applications, ensure a coherent software environment, etc.