

Heterogeneous Programming on FPGAs with OmpSs@FPGA @BSC

Friday 06 March 2020 - Friday 06 March 2020

Barcelona BSC Campus Nord

Agenda

Day 1 (Friday March, 6th)

Session 1 / 9:30am – 1:00 pm (2 h lectures, 1:30 h practical)

09.30h – Global view of OmpSs@FPGA
10:30h - Instrumentation facilities and performance analysis
11.00h - Compilation and toolchain
11:30h - Hands-on: OmpSs@FPGA installation and setup
12:00h - Hands-on: Case study: vector multiplication
13.00h – Lunch Break

Session 2 / 2:00pm – 5:30 pm (0:30 h lectures, 3 h practical)

14.00h – Hands-on: Case study: matrix multiply
15:00h - Hands-on: Heterogeneous programming using "implements"
16.30h – Hands-on: Bring your application. Study, initial analysis and recommendations
17:30h - Adjourn

END of COURSE