Session Program

Mar 9 – 12, 2020

PRACE Winter School 2020 - GPU Programming with CUDA

Memory Hierarchies in CPU/GPU Architectures

University of Innsbruck, Austria, Campus Technik, HSB 5 (ground floor)
Technikerstrasse 13 b A-6020 Innsbruck Austria

Mon, March 9

10:50 AM

12:00 PM

Memory Hierarchies in CPU/GPU Architectures

Session |

Location: University of Innsbruck, Austria, Campus Technik, HSB 5 (ground floor), Technikerstrasse 13 b A-6020

Convener: Philipp Gschwandtner